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UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Group Art Unit:
David William Boerstler	:	
	:	
Serial No.:	:	
	:	
Filed: (herewith)	:	IBM Corporation
	:	Intellectual Property Law
	:	11400 Burnet Rd.
Title: SYNCHRONIZATION STATE	:	Austin, Texas 78701
DETECTOR	:	

J1036 U.S. PTO
09/820512
03/29/01**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

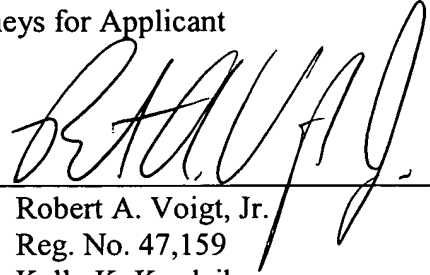
A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith, except for the pending patent applications, for which no copies are being submitted.

Respectfully submitted,

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03/09/2001 - 7047-P413US

In Place of FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANTS' INFORMATION DISCLOSURE
STATEMENT**

Serial No.:

Applicants: David William Boerstler

Filing Date: (herewith)

Group:

Atty. Docket No.: AUS920000511US1

J1036 U.S. PTO
09/820512
03/29/01

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
____ AAA	6,107,946	08/22/00	Jeong	341	101	
____ ABC						
____ ACA						
____ ADA						
____ AEA						
____ AFA						
____ AGA						
____ AHA						
____ AIA						
____ AJA						
____ AKA						
____ ALA						
____ AMA						
____ ANA						
____ AOA						
____ APA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
____ AQA						
____ ARA						
____ ASA						
____ ATA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial

____ AUA "Transceiver with 2x-Oversampling and Transmit Signal Pre-Emphasis," *IEEE International Solid-State Circuits Conference*, 1997, pp. 238-239, 464.

____ AVA "CMOS Transceiver for Serial Data Communication," *IEEE International Solid-State Circuits Conference*, 1997, pp. 242-243, 465.

____ AVA "CMOS Transceiver for Serial Data Communication," *IEEE International Solid-State Circuits Conference*, 1995, pp. 32-33, 336.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.